

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE



Inventor(s): Philip J. Kuekes et al.

Confirmation No.:

Application No.: 10/659,892

Examiner:

Filing Date: Sept. 10, 2003

Group Art Unit:

Title: Defect-Tolerant and Fault-Tolerant Circuit Interconnections

Mail Stop PGPUB Drawings
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

DRAWING TRANSMITTAL LETTER

Sir:

Enclosed herewith please find:

- () _____ sheets of redlined drawing(s) which indicate proposed changes to the drawing(s). Upon approval of these proposed changes, formal drawing(s) will be submitted.
- () _____ sheets of corrected formal drawing(s), as required by the Notice of Patent Drawings Objection (PTO-948) which accompanied the Office Action dated _____.
- () _____ sheets of corrected formal drawing(s), as required by the Notice of Patent Drawings Objection (PTO-948) and approved in the Notice of Allowability dated _____.
- (X) 45 sheets of formal drawing(s). Please substitute these formal drawing(s) for the informal drawing(s) originally filed.
- () _____

Examiner's approval of the entry of these drawings is respectfully requested. No new matter has been added.

Respectfully submitted,

Philip J. Kuekes et al.

By

Robert W. Bergstrom

Attorney/Agent for Applicant(s)

Reg. No. 39,906

Date: Dec. 11, 2003

Telephone No.: (206) 621-1933

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

Date of Deposit: Dec. 11, 2003

Typed Name: Joanne Bourguignon

Signature: